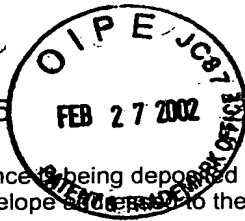


Docket No.: WMP-SME-515



0388  
#4  
8-8-02

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below.

By: [Signature]

Date: February 14, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jenoe Tihanyi  
Applic. No. : 10/007,397  
Filed : October 22, 2001  
Title : Semiconductor Component

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

German Published, Non-Prosecuted Patent Application DE 30 02 797 A1 (Kugelmann et al.), dated July 30, 1981, collector basis diode made with monolithic integrated technology;

German Published, Non-Prosecuted Patent Application DE 36 09 629 A1 (Erratico et al.), dated October 2, 1986, integrated electronic circuit for controlling inductive lasers;

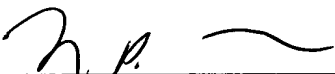
German Published, Non-Prosecuted Patent Application DE 37 32 210 A1 (Voß), dated April 13, 1989, asymmetrical thyristor;

European Patent Application EP 1 026 754 A1 (Koga et al.), dated August 9, 2000;

German Examination Report dated June 25, 2001.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

  
\_\_\_\_\_  
For Applicant

Mark P. Weichselbaum  
Reg. No. 43,248

Date: February 14, 2002

Lerner And Greenberg, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480  
Tel: (954) 925-1100  
Fax: (954) 925-1101

/bmb